



# RUTT ELECTROPHYSICS

21-29 West 4th Street, New York, N.Y., 10012 (212) 982-8300

IC-1 MC1494  
IC-2 LM 318  
IC-3 LM 318  
IC-4 LM 318

D-1 1N914  
D-2 1N914  
D-3 1N914  
D-4 1N914  
D-5 1N914  
D-6 1N914

Q-1 NPN 2N3568  
Q-2 PNP 2N3638  
Q-3 NPN 2N3568  
Q-4 PNP 2N3638

C-1 10 PF  
C-2 10 PF  
C-3 15 of 20V  
C-4 15 of 20V  
C-5 10 PF  
C-6 .1 cer  
C-7 .1 cer  
C-8 10 PF  
C-9 .1 cer

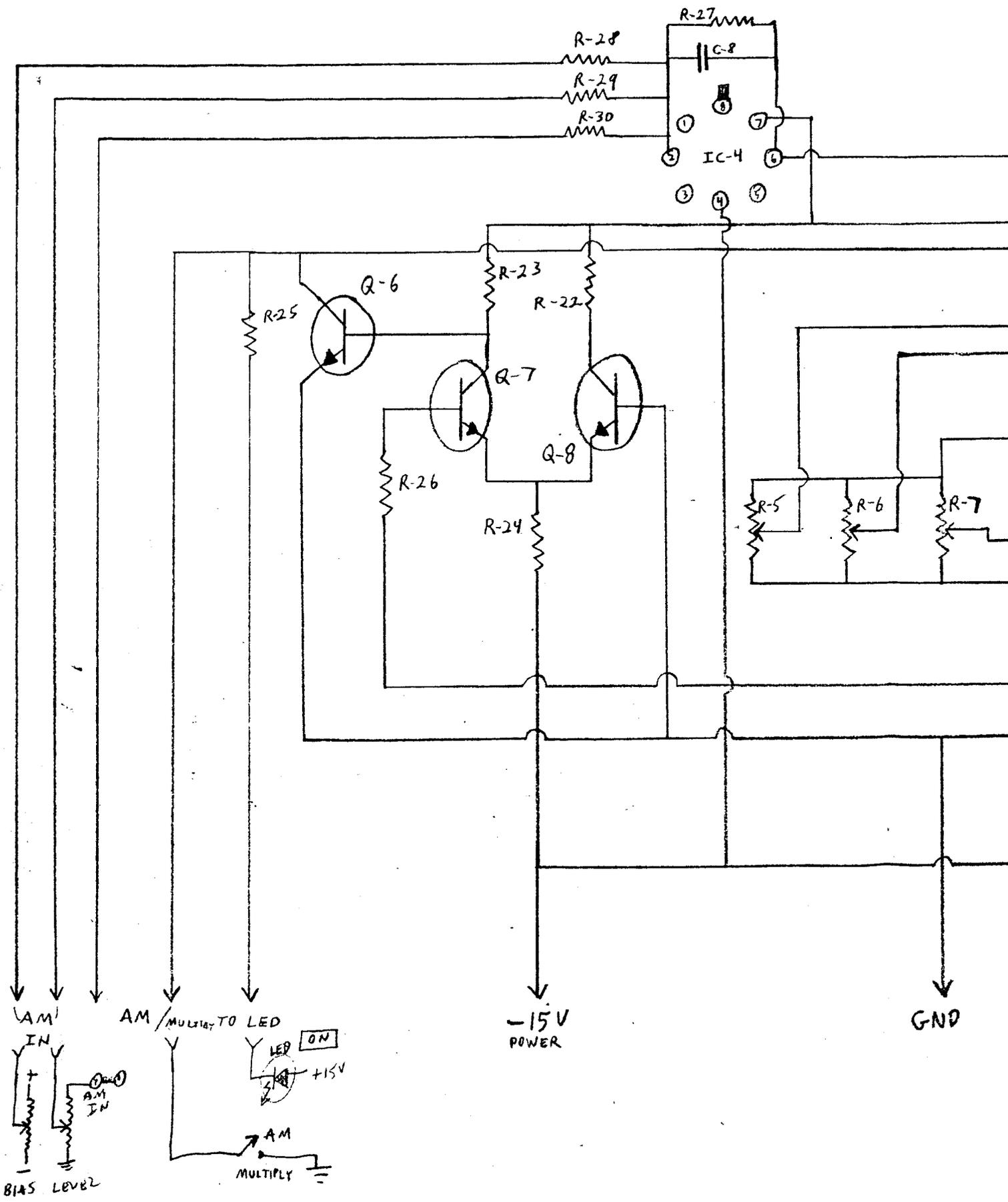
PC 127A

JAN 29, 75

Q-5 FET 2N5462  
Q-6 NPN 2N3568  
Q-7 NPN 2N3568  
Q-8 NPN 2N3568

R-1 27K  
R-2 15K  
R-3 12K  
R-4 510  $\Omega$   
R-5 20K pot  
R-6 20K pot  
R-7 20K pot  
R-8 47K  
R-9 10K  
R-10 10K  
R-11 10  $\Omega$   
R-12 10  $\Omega$

R-13 20K pot  
R-14 150  $\Omega$   
R-15 10K  
R-16 10K  
R-17 10K  
R-18 10K  
R-19 10  $\Omega$   
R-20 10  $\Omega$   
R-21 150  $\Omega$   
R-22 6.8K  
R-23 20K  
R-24 6.8K  
R-25  
R-26 10K  
R-27 20K  
R-28 15K  
R-29 10K  
R-30 2.2K  
R-31 10K  
R-32 4.7K  
R-33 omit  
R-34  
R-35 10K  
R-36 4.7K

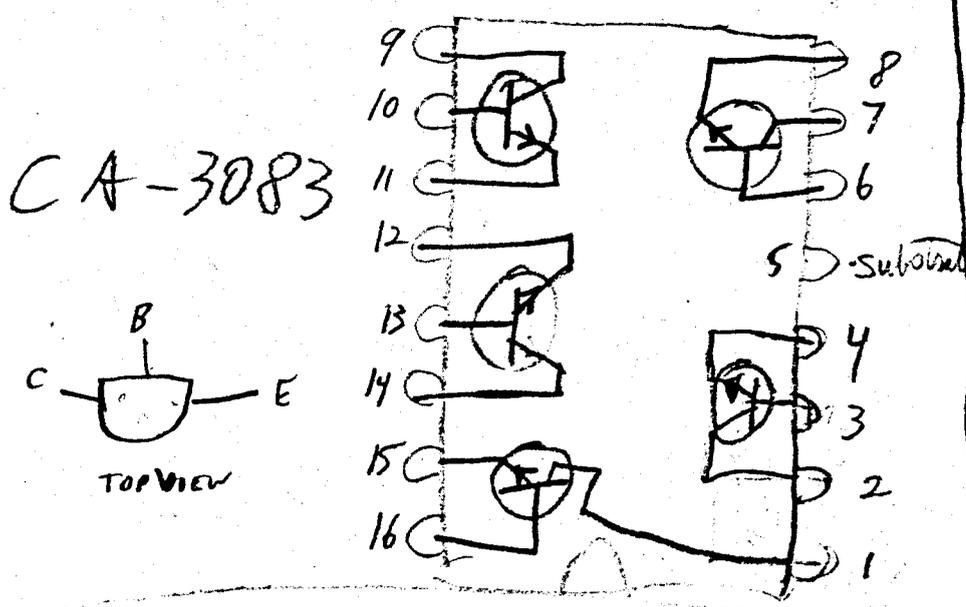




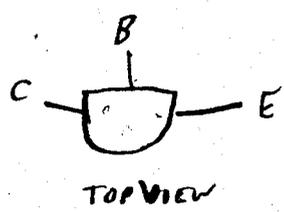
# PC-132 VIDEO DRIVER CONNECTIONS

- 1) R30, IC-10 pin 2, Q-4 base ARE NOT SUPPOSED TO GO TO
- 2) IC-10 pin 7 & OTHER COMPONENT ON SAME WIRE SHOULD ALSO GO TO -15V
- 3) R-44 IS NOT GROUNDED

21) Change G-1 FROM 070-28 TO I15  
 22) connect R8 & R10 ON IC+Ic-3



CA-3083

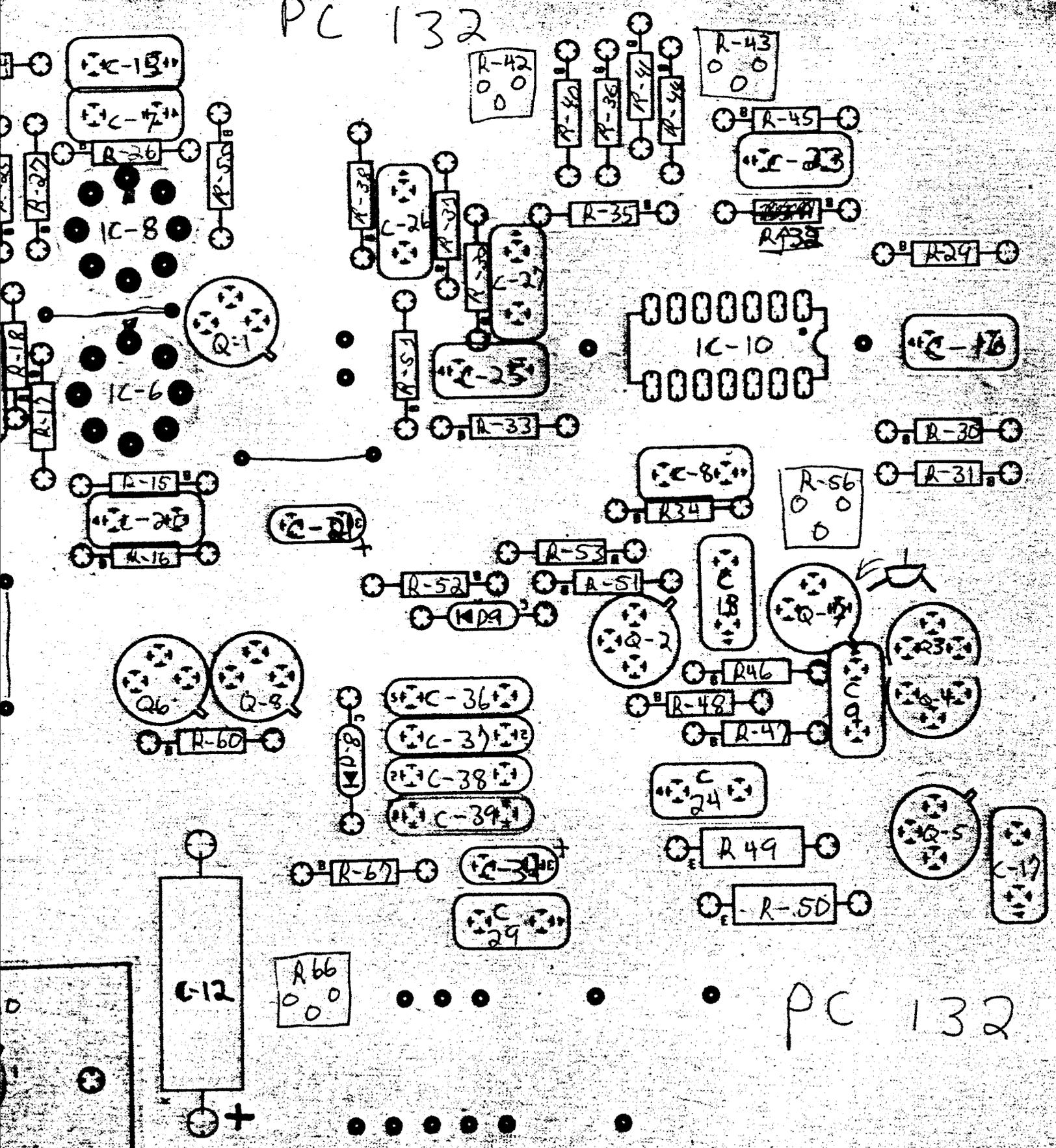


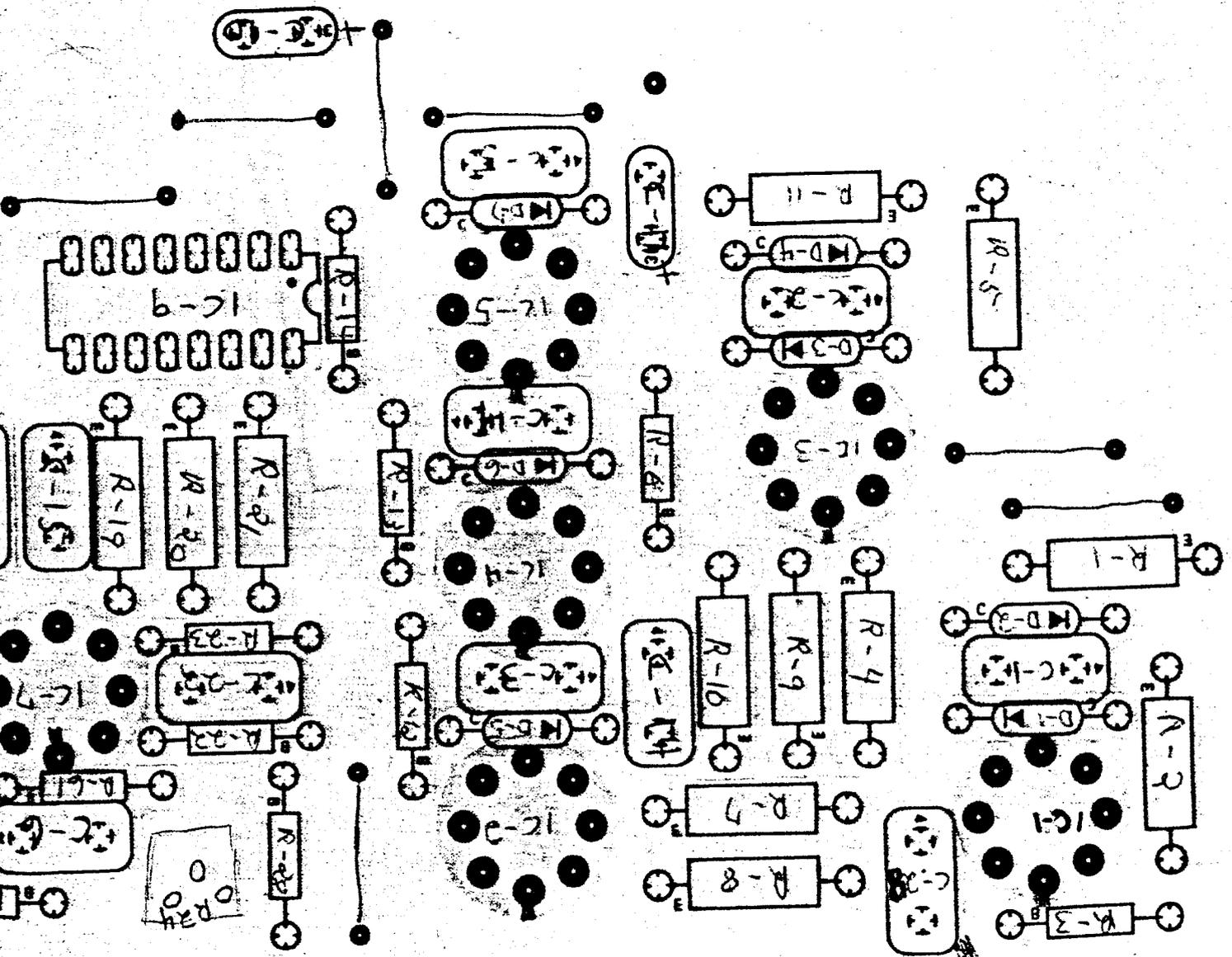
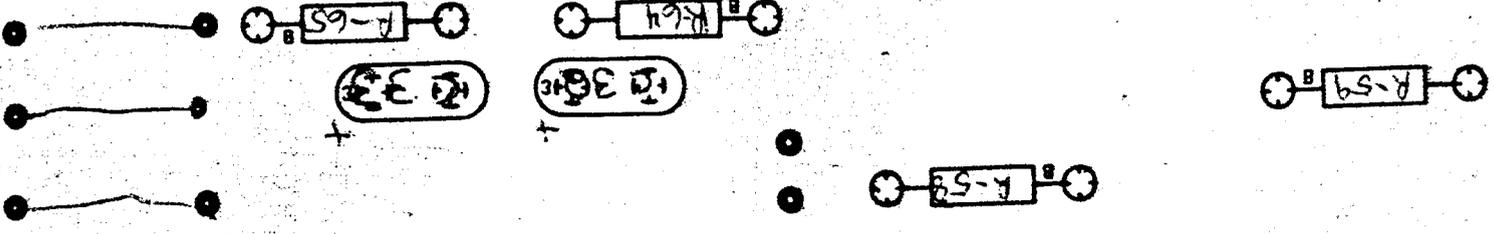
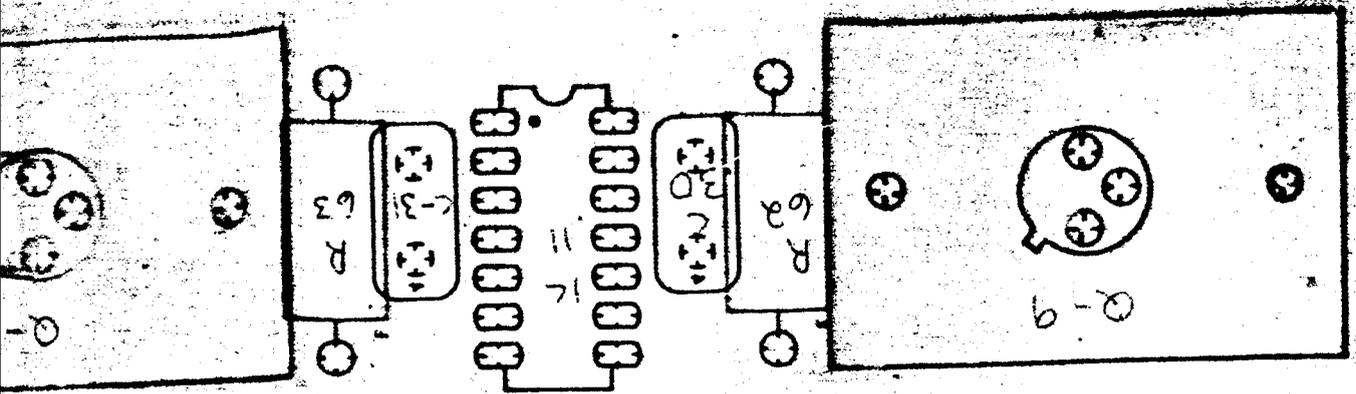
- 19) Reduce VALUE of R61
- 20) PUT LIMIT POT ON INTENSITY (OCU) & BIAS POT TO SET EXT INT. AS SPECIFIC
- 21) ADD Resistor in series with +45V in
- 15) ADD 10K CAP TO IC-8 pin 2-6
- 16) ADD 330 RESISTORS INSTEAD OF CAPACITORS ON +45V TO 564501
- 17) PUT 470K Resistor IN SERIES WITH CAPACITORS
- 18) ADD 10K Res IN SERIES W/6-2

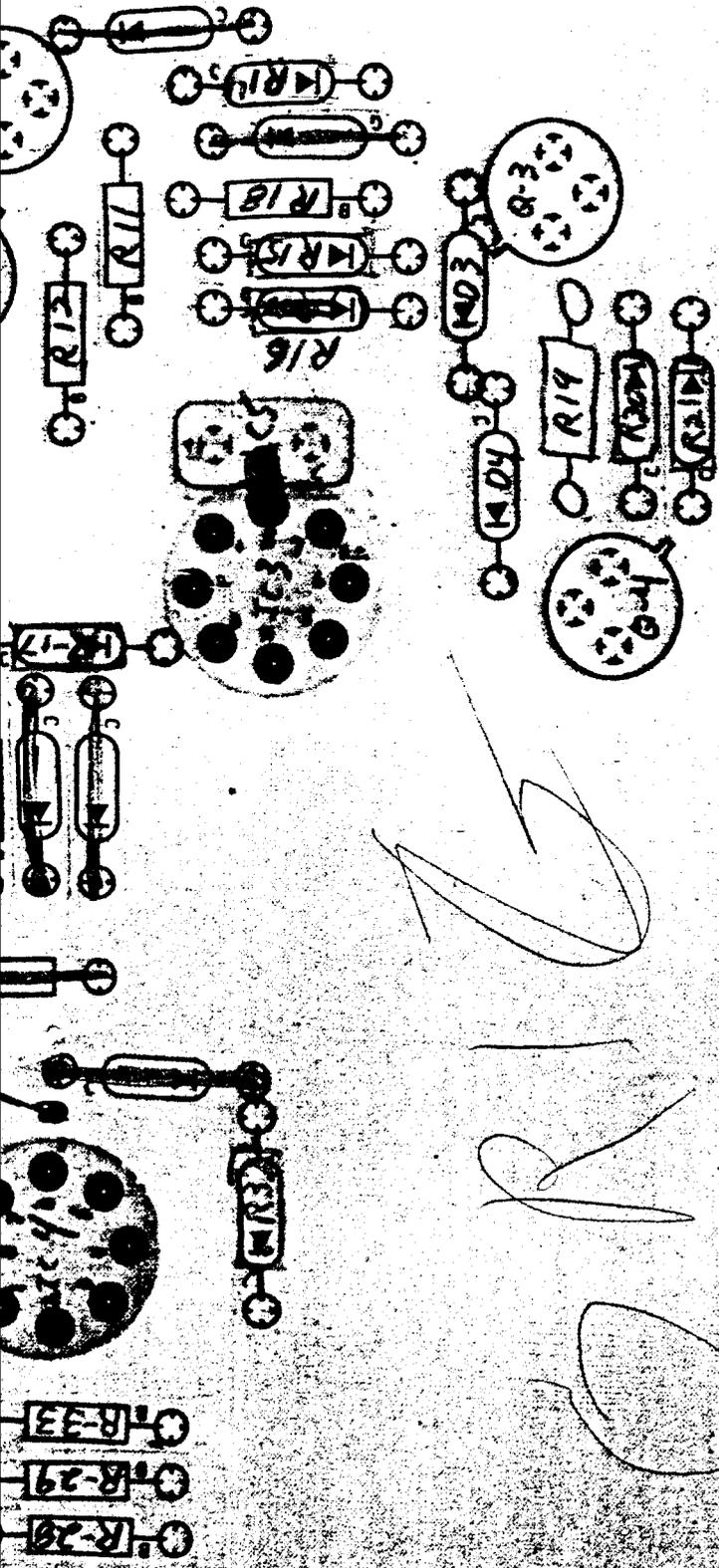
- 4) IC 2 Pin 2 & 4 are MESSSED UP  
 CONNECT PIN 4 TO B-  
 CUT B- FROM PIN 2 & CONNECT PIN 2 TO 05+23

OVER

PC 132







0.C-9.

